

Docket No. 501.43313X00
Serial No. 10/743,706
June 27, 2005

AMENDMENTS TO THE CLAIMS:

The following listing of claims replaces all prior listings, and all prior versions, of claims in the application.

LISTING OF CLAIMS:

1. (Canceled)

2. (Currently Amended) A display device comprising:

~~including a semiconductor layer formed on~~

~~a substrate; and having pseudo single crystal regions; and~~

a plurality of thin film transistors, wherein each of the thin film

transistors includes a ~~arranged inside of the pseudo single crystal regions;~~

~~wherein:~~

~~in the pseudo single crystal region, the semiconductor layer includes crystals which are grown in an elongate strip-like shape in a direction parallel to the substrate, and~~

of the plurality of thin film transistors, two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, have the direction of the length of gates of the respective thin film transistors arranged with an inclination of within ± 20 degree with respect to the longitudinal direction of the strip-like grown crystals, and are arranged such that, when channel regions of the respective thin film transistors are imaginarily extended in parallel to the growth direction of the strip-like grown crystals, at least portions of the channel regions are superposed on each other.

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3. (Currently Amended) A display device according to claim 2, wherein a ~~extent~~ an extent of superposition of the channel regions is 50% or more.

4. (Currently Amended) A display device according to claim 3, wherein a ~~extent~~ an extent of superposition of the channel regions is 80% or more.

5-10. (Canceled)

11. (Previously Presented) A display device according to claim 2, wherein two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, are formed of a differential pair of transistors which constitute a differential amplifying circuit.

12. (Previously Presented) A display device according to claim 2, wherein two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, are formed of a pair of transistors of an active load circuit which constitutes a differential amplifying circuit.

13. (Previously Presented) A display device according to claim 2, wherein two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, are formed of a pair of transistors of an active load circuit which constitutes a differential amplifying circuit and a transistor having a gate thereof to which an output voltage of the active load circuit is applied.

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14. (Previously Presented) A display device according to claim 2, wherein two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, are formed of a pair of transistors which constitute a current mirror circuit.

15. (Previously Presented) A display device according to claim 2, wherein two or more thin film transistors, which are required to exhibit small irregularities relative to each other as characteristics of the transistors, are connected in parallel to each other thus equivalently constituting one transistor.

16-20. (Canceled)

21. (New) A display device according to claim 2, wherein the plurality of thin film transistors are formed on a flat surface.